

Vidya Prasarak Mandal's **Maharshi Parshuram College of Engineering, Velneshwar** At: Velneshwar, Hedvi-Guhagar road, Taluka: Guhagar, Dist: Ratnagiri (Maharashtra) 415 729

(AICTE & DTE approved and affiliated to University of Mumbai)

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CRITERION 6 – Governance, Leadership and Management

Key Indicator -6.3 Faculty Empowerment Strategies

6.3.3 Average number of professional development /administrative training programs organized by the institution for teaching and non teaching staff during the last five years

Sr.No	Metric. No	Document Details	Academic Year
1	6.3.3	Mechatronics with emphasis on solar applications	2013-14
2		Workshop on PLD and Recent Trends	



VPM's Maharshi Parshuram College of Engineering, Velneshwar Mechatronics with emphasis on solar applications

Workshop 'Mechatronics with emphasis on solar applications' conducted from 12th July 2013 to 19 July 2013

Introductory session was conducted on 12th July 2013 by Dr K.D. Desai (Director VPMMPCOE Velneshwar)

Following Training topics discussed with the Faculties:

- ✓ Solar Energy
- ✓ Basics of Electronic equipments and devices
- ✓ Solar Energy applications
- ✓ Solar appliances
- ✓ Renewable energy sources

A] Some of these applications included:

- 1. Remote power for villages
- 2. Water pumping
- 3. Water purification
- 4. Vaccine refrigeration
- 5. Electric fences
- 6. Telecommunications
- 7. Remote farms and homes
- 8. Signaling
- 9. Industrial applications
- 10. Disaster relief

B] Other uses:

Solar electricity is increasingly appearing in more and more consumer devices driven by reducing cost, increasing performance and reducing energy demands in small electronic devices. Solar electricity is increasingly used in the following applications:

- 1. Camping, caravanning and boating
- 2. Recharging mobile phones
- 3. Powering laptop computers
- 4. Watches and calculators
- 5. Street lighting
- 6. Isolated vending machines
- 7. Bill board lighting
- 8. Security systems

C] Benefits of solar energy

- Using the power of the sun to heat and light our houses is a very clean and environmentally friendly way of generating power.
- Installing solar power in your home will guarantee that you have much lower fuel bills almost instantly.
- These systems can fit into existing buildings and it does not affect land use



Vidya Prasarak Mandal's Maharshi Parshuram College of Engineering, Velneshwar

Attendance sheet

Academic Year: 2013-14

Name of the programme/event: Mechatronics with emphasis on solar applications.

Date of programme/event: 12/07/2013 - 19/07/2013

Objective of the programme: To form collaboration between electronics and mechanical engineering department to develop some useful projects for society like solar projects.

Recourse Person: Dr. K.D. Desai

Department Name: Electronics & Telecommunication

Participant's Name	Participant's Name	Participant's Name
Dr .Shilpa	Mr. Vaibhav	Mr.Aniket Akhade
Deorukhakar	Ambavkar	
Mr. Balagauda Patil	Mr.Manish Prabhu	Mr. Siddhesh Kamat
Mr. Ashish Chaudhari	Mr.Balaji rodage	Mr.Aditya prabhukhot
Mr.Shoebuddin	Mr.Santosh Shinde	Mr. Yogesh
Madani		Prabhutendolkar
Ms.Rasika Burate	Mr.Avinash Pawar	Mr.Kailash Karnekar
Ms.Dhanashree	Mr.Sachin Karad	Mr. Vikram Shinde
Hindalekar		
Mr. Abhishek Anande	Mr. Niranjan	
	Samudre	

Mr. Avdhut Aapte HOD MPCOE EN3462 MPCOE EN3462

Dr. Avinash Chincholkar Principal



VPM's MAHARSHI PARSHURAM COLLEGE OF ENGINEERING

Hedvi Guhagar Road, At : Velneshwar, Taluka Guhagar, District : Ratnagiri, Maharashtra 415 729

A Workshop and Exhibition on "Mechatronics with emphasis on Solar applications" Organized during July 12 to July 19, 2013

Certificate of Appreciation

This certificate is awarded to Mr./Mrs./Ms. Amboukar Vaibbar. a Faculty / Staff member of this college, in recognition of his / her valuable contribution as Member of organizing committee in Workshop and Exhibition on "Mechatronics with emphasis on Solar applications" during July 12 to July 19, 2013.

Principal

Dr. K. D. Desai Director



Maharshi Parshuram College of Engineering

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Report on Workshop on PLD and Recent Trends

(Held on: 21st October 2013)

DATE: 21/10/2013

☐ Venue: EXTC Department Classroom

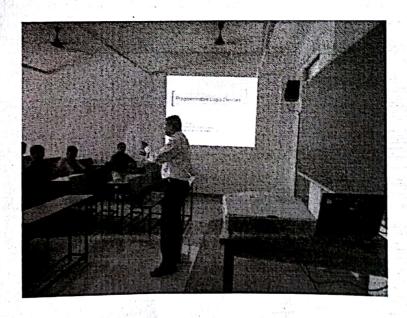
☐ Time:10am-4pm

Today department of Electronics & Telecommunication has successfully conducted the workshop on "Programmable Logic Devices and Recent Trends", by Mr. Vpul J. Gohil from Q-logic, Pune. Workshop was arranged in E&Tc Department classroom. Workshop was started on the given time to the guest i.e. at 9.15 am. All the final year students from Div A as well as Div. B were present for the lecture.

Speaker has discussed about the PLA structure in the detail. CPLD and FPGA architectures were highlighted. Furthermore speaker discussed about the implementation of the program in hardware. Basic building blocks of the architecture were briefed during the workshop.

Presenter was enthusiastic to discuss about the faculty queries, and highlighted about the prototyping concept used in the industry for new product. Some innovative project was shared to the faculty by speaker.

Workshop was end by 4.00 pm concluded by Ms. Dhanashree Hindalekar Workshop Coordinator by vote of thanks.



Ms. Dhanashree Hindalekar
Workshop Coordinator

Mr. Avdhut Apte H.o.D.



Vidya Prasarak Mandals

Maharshi Parshuram College of Engineering

Electronics and Telecommunication

Attendance Sheet for Workshop on PLD and Recent Trends

Date-21/10/2013

Sr.No	Name of Faculty	Signature of faculty	
1	Mr.Avdhut Apate	Malo	
2	Mr.Sandip Dwivedi	202	
3	Mr.Kaustubh Joshi	Myrus In	
4	Miss.Rasika Burate	P. Bruh	
5	Miss.Dhanashree Hindalekar	pro phyrast Nyth	

Workshop Coordinator

Miss.Dhanashree Hindalekar

Mr.Avdhut Apate